HFFT-01A FPGA (Virtex™ Ultrascale+) Based Ultra Low Latency HFT Solution

Highlighted Features

- Optimized SWs for ultra low and deterministic latency (Parser, Synchronizer, Order Book Generation, Trigger Generation, Order Generation, TCP)

- Supports both HFT Tick-to-Trade and Accelerator applications simultaneously for different trading approaches

- **FPGA-Based Ultra Low Latency Tick-to-Trade**
- **FPGA-Based Ultra Low Latency Accelerator**

General System Level Features

- FPGA Based HW Module processes two ITCH feeds simultaneously
- Generates single ITCH feed by combining primary and secondary feeds for accelerating WS trading algorithms
- Multiplexing two additional order lanes (unlimited TCP sessions) with FPGA orders to support fastest switch free operation
- Recording control messaging, market data feeds, inbound and outbound orders with time stamp for analysis and back testing

Tick-to-Trade Features

- Optimized for ultra-low and deterministic latency (Parser, Synchronizer, Order Book Generation, Trigger Generation, Order Generation, TCP)
- Built-in algorithms running on FPGA generate orders with minimum delay
- Customer trading algorithms (under NDA) can also be implemented on FPGA for ultra-low latency T2t trading
- Up to 120 instruments per FPGA can be supported
- Supports multiple TCP sessions for order generation
- Supports both HFT Tick-to-trade and Accelerator applications simultaneously

Accelerator Features

- Optimized for ultra-low and deterministic latency (Parser, Synchronizer, Order Book Generation, Trigger Generation)
- Price changes are sent to WS together with other information including current quantities
- SolarFlare NIC used for direct user space DMA
- Delay-optimized SW Module receives price updates and triggers trading algorithms
- HW Module creates the trigger in nanoseconds after the ITCH command causing it. SW Module triggers Trading Software with a minimum possible delay depending on WS performance

Adhoc Teknoloji A.Ş. is a startup engineering company located in Ankara, Türkiye. The company specializes in high-performance communication solutions, driven by its highly motivated engineering team.

Adhoc Teknoloji A.Ş. is focused on High Frequency Trading Solutions, including FPGA-based Tick-to-Trade systems, Accelerators, Fast Software Order Book Generation, Market Data Simulator, Market Order Simulator and FPGA-based Precision Network Measurement & Analysis Solution.
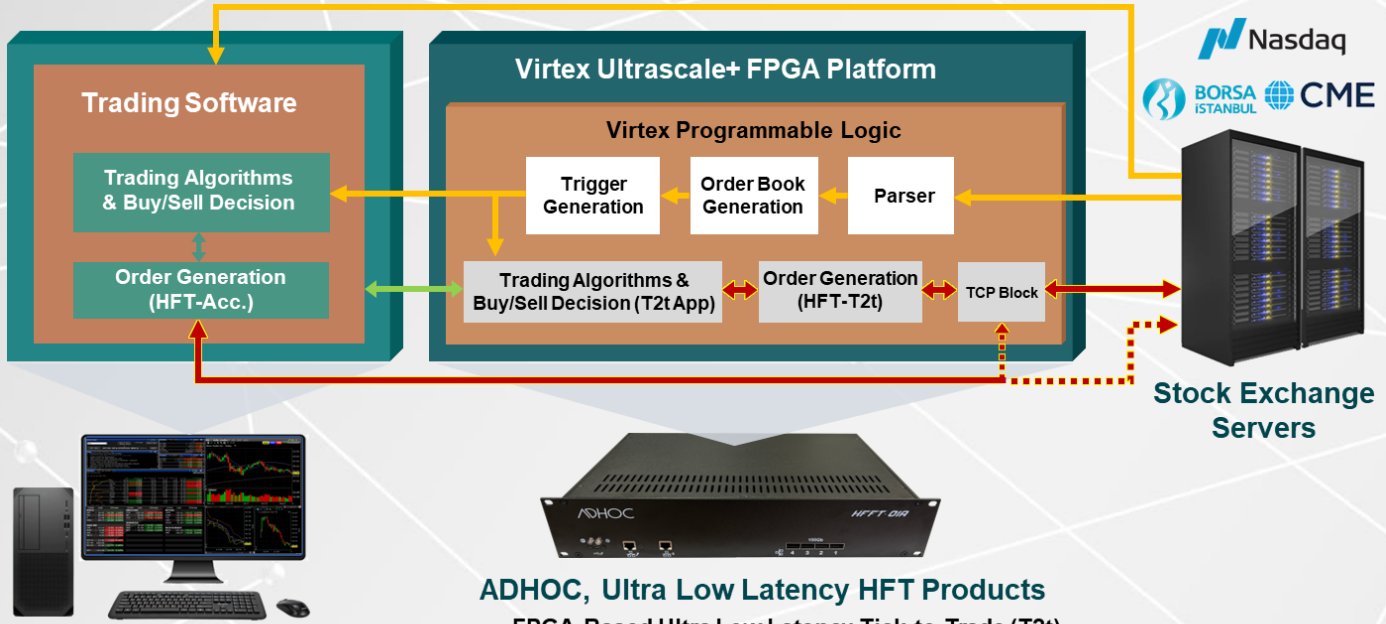
Virtex Ultrascale+ FPGA Platform

Trading Software
- Trading Algorithms & Buy/Sell Decision
- Order Generation (HFT-Acc.)

Virtex Programmable Logic
- Trigger Generation
- Order Book Generation
- Parser
- Trading Algorithms & Buy/Sell Decision (T2t App)
- Order Generation (HFT-T2t)
- TCP Block

Stock Exchange Servers

Trader Workstation

ADHOC, Ultra Low Latency HFT Products
- FPGA-Based Ultra Low Latency Tick-to-Trade (T2t)
- FPGA-Based Ultra Low Latency Accelerator (Acc)

Interfaces

QSFP28 Ports	• 4x QSFP28 with x16 GTY supporting to 16x10/25 (4x40/100) Gbps
RJ-45 Ports	• 1x 10/100/1000 Mbps Ethernet System Debug and Control • 10/100 Mbps Ethernet to Platform Management Board Power Monitor and Control
Micro USB Port	• Console Serial Port
Indicators	• Info RGB LED for Power Status and DONE • RGB LED for Communication Link Status
Power ON/OFF	• ON/OFF switch

Power Supply Specifications

Reliability	• Redundant Power Supply
Input Voltage	• 85-264 VAC, typical, 115/230 V • 120-373 VDC (optional)
Input Frequency	• 47-63 Hz, typically 50/60 Hz
Input Connector	• IEC 60320 C14 Filtered/Fused Inlet
Power Consumption	• Less than 50W
Management	• Platform Management Board monitors and controls the system

Accessories

Power Cables	• 2 x AC Power Cable
Other Cables	• F/O and DAC QSFP cables • Ethernet Cables • Micro USB Cable

Mechanics

Form Factor	• 2U 19" Rackmount Form Factor
Size (W x H x D)	• 482.6 x 88.0 x 206 mm (W/Mounting Ears)
Active Cooling	• Single Fan with deducted airflow design

Support Services

- Highly motivated and skilled engineering team for custom development
- Support for customer SW algorithm development for best performance
- Measurement of performance for optimization
- Support for selection & tuning of Workstation for Accelerator application

Customization

- ADHOC can also implement your indigenous trading algorithms directly on FPGA for ultra low latency T2t trading
    - Algorithms will not be shared with any other customer